

ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1

Stylesheet Version v1.1.1

Title of Invention

METHOD AND STRUCTURE FOR IMPROVING CMOS DEVICE
RELIABILITY USING COMBINATIONS OF INSULATING
MATERIALS

Submission Type :

Utility Patent Filing

Application Number:

10/711897



EFS ID:

70292

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First Named Applicant:

Haining Yang

Attorney Docket Number:

FIS920040194US1

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